Assessment, Improvement, Measurement (AIM) Report: 10/08/2013

Plan Year: 2012-2013
Unit: Engineering and Electronics Technology
Coordinator(s): Kevin Dunn, Karman Wheeler, Paul Turner
Reviewer: Paul Turner

Objective or Outcome	Measure(s)				
	Measure Text	**Achievement Target**	**Results**	**Achievement Target Result**	**Use of Findings/Next Steps**
SLO 1 - Students will demonstrate the design, construction, and troubleshooting of simple circuits using combinatorial and sequential logic. Calculating, designing, drawing, simulating, and creating a design portfolio of a BCD to 7-segment decoder circuit in the Digital course, will complete student demonstration.	Project - The project will be graded on a 100-point evaluation instrument based on how well the circuit is designed, drawn as a schematic, built as a simulated circuit, and described in a written portfolio. This goal will be met when 90% of students score a 80 or higher on the BCD to 7-segment decoder circuit rubric.	90% of students will score a 80% or higher on the project.	100% of students scored 90% or higher on the project	Met	The students met our target level of achievement but faculty felt one of the weaker areas for the students was related to use of the software. We will continue to monitor all student projects for content and purpose. while providing more time and demonstrations on use of the software. I full course period will be dedicated to software usage during the next semester.
SLO 2 - Students will demonstrate an understanding of the multi-stage amplifier circuit to include focus on their design and written description of the circuit. Skills will be demonstrated by the design of a multi-stage amplifier and a written description of the various section of the circuit during the Devices 2 course. Special emphasis is being placed on the design and written description of the circuit.	Project - The project will be graded on a 100-point evaluation instrument based on how well the circuit is designed, drawn as a schematic, built as a simulated circuit, and described in a written portfolio.	90% of students will score an 80% or higher on the project.	98% of students scored an 80% or higher on the project.	Met	The students met our target level of achievement, but faculty felt one of the weaker areas for the students was related to the written portion. We will continue to monitor all student projects for content and purpose. while providing more time and demonstrations on best practices in writing a technical report. Even though it was weaker, students are still strong in this area and we feel no further action needs to be taken.
SLO 3 - Students will calculate, construct, plot, and measure RL and RC series circuit and phase	Project - The project will				No further planning on

shift circuits. Calculating, accurately plotting, measuring the phase between voltage and current at different frequencies, and describing a RL and RC series circuit with increased emphasis on the operation of the circuits.	be graded on a 100-point scale based on how accurately the circuit is calculated, built as a live circuit (operational), and described in a written paper.	90% of students score an 80% or higher on the RL and RC series circuit.	92% of students scored an 80% or better on the project.	Met	this task will take place. We have an assessment exam in place and hope to expand upon the needs of those results for future planning.