

Assessment, Improvement, Measurement (AIM) Report: 12/18/2013

Plan Year: 2013-2014

Unit: Engineering and Electronics Technology

Coordinator(s): Kevin Dunn, Karman Wheeler, Paul Turner

Reviewer: Paul Turner

Objective or Outcome	Measure(s)		
	Measure Text	Achievement Target	Assess Month
SLO 1 - Students will demonstrate the design, construction, and troubleshooting of simple circuits using combinatorial and sequential logic. Calculating, designing, drawing, simulating, and creating a design portfolio of a BCD to 7-segment decoder circuit in the Digital course, will complete student demonstration.	Project - The project will be graded on a 100-point evaluation instrument based on how well the circuit is designed, drawn as a schematic, built as a simulated circuit, and described in a written portfolio. This goal will be met when 90% of students score a 80 or higher on the BCD to 7-segment decoder circuit rubric.	90% of students will score a 80% or higher on the project.	May
Students will demonstrate an understanding of transistor operations in Solid State Circuits (Task 21).	NOCTI scores for task 21.	This goal will be met when the score for task 21 has increased 10%. The current score of 36.0 should increase to 39.6 to be MET.	May
Students will be able to correctly utilize a reference manual in the area of Digital Theory. (Task 39)	NOCTI score on task 39	This goal will be met when the score for task 39 has increased 10%. The current score of 33.3 should increase to 36.6 to be MET.	May