

**Assessment, Improvement, Measurement (AIM) Report: 04/03/2013****Plan Year:** 2012-2013**Unit:** Engineering and Electronics Technology**Coordinator(s):** Kevin Dunn, Karman Wheeler, Paul Turner**Reviewer:** Paul Turner

Objective or Outcome	Measure(s)		
	Measure Text	Achievement Target	Assess Month
SLO 1 - Students will demonstrate the design, construction, and troubleshooting of simple circuits using combinatorial and sequential logic. Calculating, designing, drawing, simulating, and creating a design portfolio of a BCD to 7-segment decoder circuit in the Digital course, will complete student demonstration.	Project - The project will be graded on a 100-point evaluation instrument based on how well the circuit is designed, drawn as a schematic, built as a simulated circuit, and described in a written portfolio. This goal will be met when 90% of students score a 80 or higher on the BCD to 7-segment decoder circuit rubric.	90% of students will score a 80% or higher on the project.	December
SLO 2 - Students will demonstrate an understanding of the multi-stage amplifier circuit to include focus on their design and written description of the circuit. Skills will be demonstrated by the design of a multi-stage amplifier and a written description of the various section of the circuit during the Devices 2 course. Special emphasis is being placed on the design and written description of the circuit.	Project - The project will be graded on a 100-point evaluation instrument based on how well the circuit is designed, drawn as a schematic, built as a simulated circuit, and described in a written portfolio.	90% of students will score an 80% or higher on the project.	March
SLO 3 - Students will calculate, construct, plot, and measure RL and RC series circuit and phase shift circuits. Calculating, accurately plotting,	Project - The project will be graded on a 100-point scale based on how accurately the circuit is calculated, built as a live circuit (operational), and described in a written paper.	90% of students score an 80% or higher on the RL and RC series circuit.	May

measuring the phase between voltage and current at different frequencies, and describing a RL and RC series circuit with increased emphasis on the operation of the circuits.			
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